



PATENT
DOCKET NO.: 2685/6006



THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Alexander FRASER
SERIAL NO. : 10/053,023
FILED : 18 January 2002
FOR : SYSTEM AND METHOD FOR HANDLING
PRIORITIZED DATA IN A NETWORK
GROUP ART UNIT : 2661

COMMISSIONER FOR PATENTS
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Alexandria, Virginia 22313-1450

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**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. § 1.97 & § 1.98**

S I R:

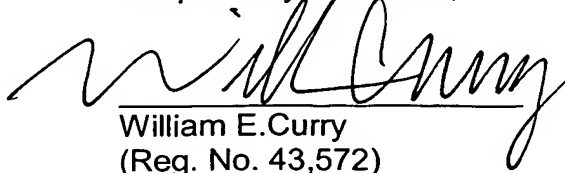
In conformance with Applicant's duty of disclosure under 37 C.F.R. § 1.56 and § 1.97(b)(3), the references listed on the attached form PTO-1449 are hereby brought to the Examiner's attention.

The references were cited in the International Search Report of PCT/US03/011089. Copies of the references are enclosed, together with a copy of the Search Report.

This Information Disclosure Statement is being filed prior to issuance of a first Office Action on the merits in this application.

It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear in the "references cited" on any patent to issue therefrom.

Respectfully submitted,


William E. Curry
(Reg. No. 43,572)

Date: 22 May 2003

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FORM PTO-1449
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT(S)

Atty Docket No.: 2685/6006
 Serial No. : 10/053,023
 Applicants : Alexander Fraser
 Filed : 18 January 2002
 Group Art Unit : 2661
 Examiner :



U.S. PATENT DOCUMENTS

Examiner Initial	Patent Number	Patent Date	Name	Class/ Subclass	Filing Date
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FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class/ Subclass	Translation Yes No
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	0 684 719	11/29/95	EPO		N/A
	1 128 612	08/29/01	EPO		N/A

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner
Initial

- | | | |
|-------|----|--|
| _____ | 1. | Naoya Watanabe et al: A Preemptive Packet Transfer Scheme with Virtual Cells in a Long Packet, ELECTRONICS & COMMUNICATIONS IN JAPAN, PART I - COMMUNICATIONS, SCRIPTA TECHNICA, Vol. 71, No. 2, 1 Feb. 1988, pp. 71-80 |
| _____ | 2. | HASHEMI M R ET AL.: The Single-Queue Switch: A Building Block for Switches with programmable Scheduling, IEEE JOURNAL ON SELECTED AREAS IN COMMUNICATIONS, IEEE INC., New York, US, Vol. 15, No. 5, 1 June 1997, pp. 785-794 |
| _____ | 3. | ADJUSTABLE DUAL PRIORITY INTERRUPT SWITCHING METHOD, IBM TECHNICAL DISCLOSURE BULLETIN, IBM CORP., NEW YORK, US, Vol. 37, No. 8, 1 August 1994, page 649 |

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.